

1/5

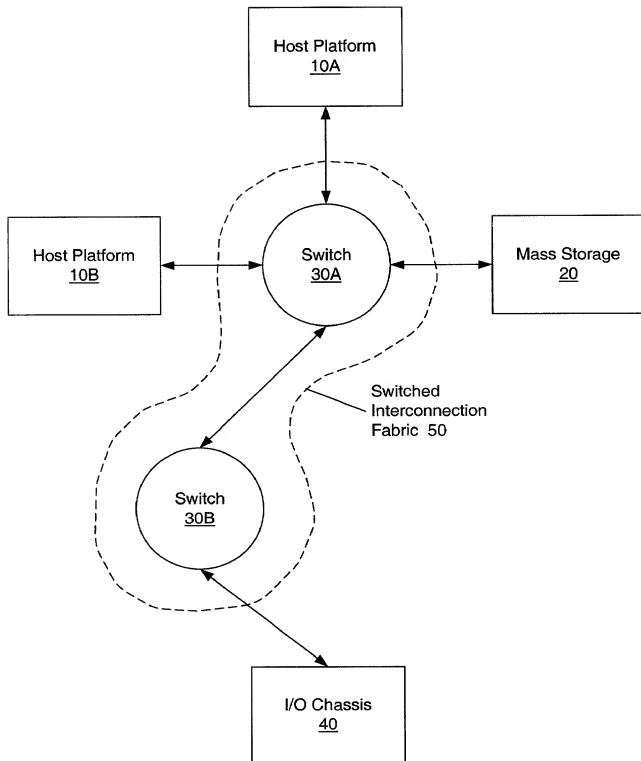


FIG. 1

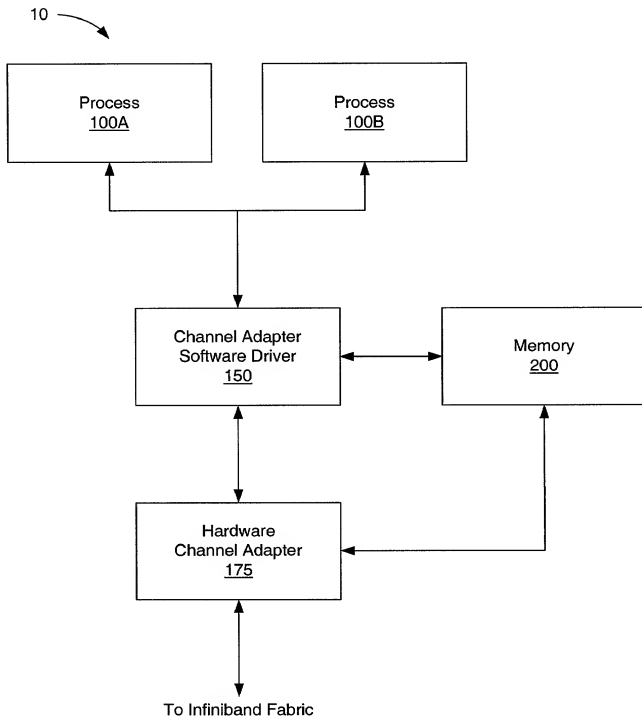


FIG. 2

QP1_Next_VA_REG	<input type="text"/>
QP1_HW_Doorbell_REG	<input type="text"/>
QP1_In_Service_Bit	<input type="text"/>
QP2_Next_VA_REG	<input type="text"/>
QP2_HW_Doorbell_REG	<input type="text"/>
QP2_In_Service_Bit	<input type="text"/>
	⋮
QPx_Next_VA_REG	<input type="text"/>
QPx_HW_Doorbell_REG	<input type="text"/>
QPx_In_Service_Bit	<input type="text"/>

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FIG. 3A

Virtual Address	Work Queue Element Information	Next Virtual Address	Next WQE Posted	Busy
WQE_VA0		0	0	0
WQE_VA1		0	0	0
WQE_VA2		0	0	0
WQE_VA3		0	0	0
QPx Driver Last WQE Accessed Register <u>215</u>		QPx 210		

FIG. 3B

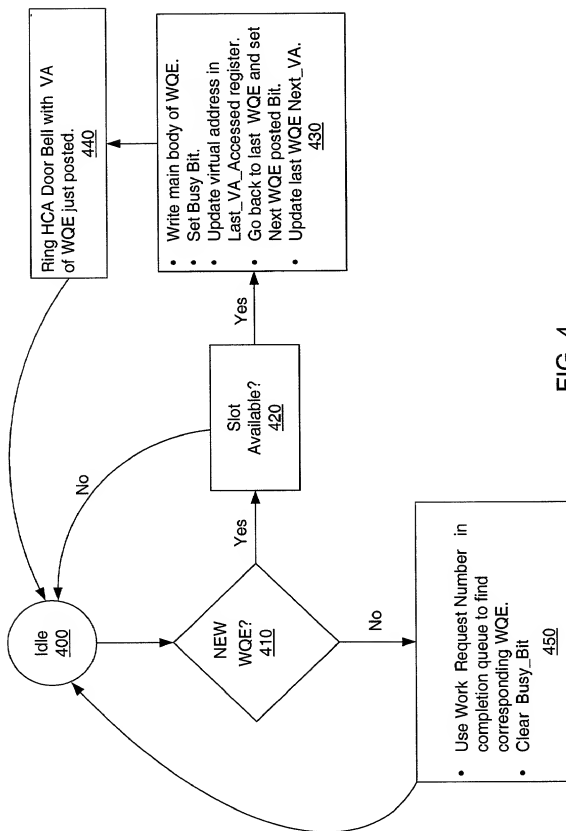


FIG. 4

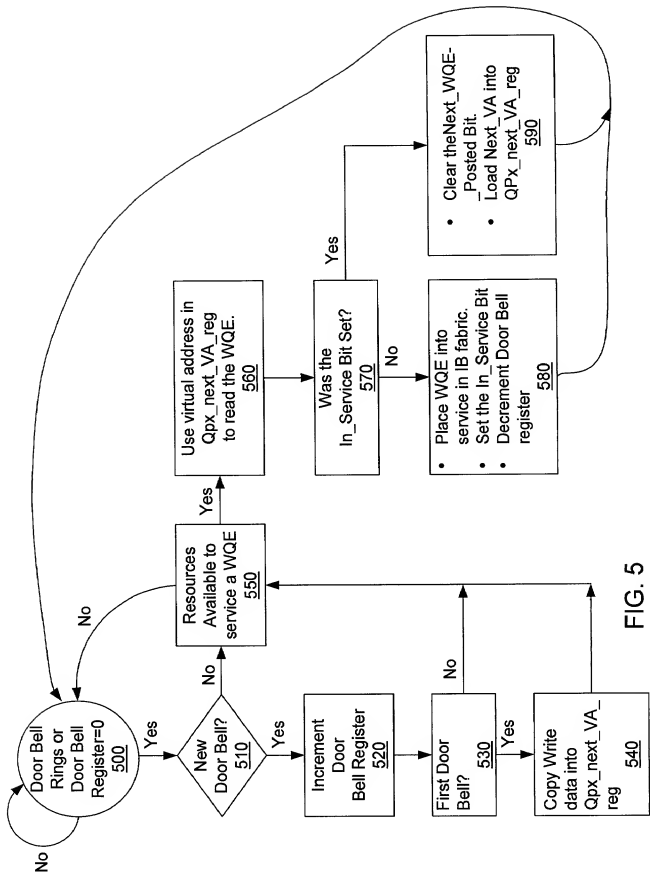


FIG. 5